# RELIABILITY QUALIFICATION REPORT FOR LEAD-FREE/RoHS-COMPLIANT/GREEN SOT-89 PACKAGED RF MESFET/HFET SEMICONDUCTORS 

## I. SUMMARY

The SOT-89 package using MESFET/HFET devices has been lead-free/RoHS qualified to a maximum reflow profile of $260^{\circ} \mathrm{C}$, and the MSL rating at this reflow profile is level 3. The lead finish is NiPdAu. The AH102A-G was selected to qualify the MESFET/HFET amplifier family of devices in the SOT-89 package because it is the most complicated device, has the highest DC power consumption, and the highest current density of the MESFET/HFET SOT-89 amplifier family. The parameters monitored for the qualification tests were Supply Current, Gain and OIP3. Failures are defined as any variation of $10 \%$ or greater for Supply Current, a variation of 1 dB or greater for Gain \& a variation of 2 dB or greater for OIP3.

## II. SCOPE

This report summarizes the reliability qualification of the AH102A-G. Other devices that are qualified by similarity at the time of this published report are: AG101-G, AG102-G, AG103-G, AH1-G, AH1-1G, AH2-G, AH3-G, AH31-G, AH101-G, AM1-G, FH1-G, FH101-G, FP101-G, FP1189-G, and FP2189-G. The Application Note "453654 Solderability Test Report for WJ Products With Lead-Free Package Finish" has a detailed description of the lead-free solderability tests; results of the solderability testing are shown in Section IV. The reliability data are obtained through the performance of specified accelerated stress tests described in this document.

## III. APPLICABLE DOCUMENTS

All the test procedures and test methods are consistent with industry standards. The standards referenced in this document are JEDEC standard 22.

## IV. QUALIFICATION TEST PLAN

| Stress or Test | Procedures/Conditions | Device Hours/ Cycles | Sample Size | Failed Units | Reference Document | Part Tested |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Preconditioning Level 3 Lead Free | External visual 40x <br> High Temp. Storage Life 24 hrs $@+125^{\circ} \mathrm{C}$ <br> Temp. \& Humidity Test 192 hrs. @ $+30^{\circ} \mathrm{C} / 60 \% \mathrm{RH}$ <br> Convection Solder Reflow test 3 cycles w/flux immersion, peak temperature $260^{\circ} \mathrm{C}$ | N/A | 3 lots, a total of 624 parts | 0 | JESD22-A113D JESD22-A101-B JESD22-B101A JESD22-A103C J-STD-020C | AH102A-G |
| Temperature Cycle | $\begin{array}{\|l\|} \hline \text { Test Condition } \mathrm{C} \\ \text { Temp. }-65^{\circ} \mathrm{C}\left(+0^{\circ} /-10^{\circ} \mathrm{C}\right) \text { to }+150^{\circ} \mathrm{C} \\ \left(+10^{\circ} /-0^{\circ} \mathrm{C}\right) \\ \text { Dwell time }=10 \text { to } 15 \mathrm{~min} . \\ \hline \end{array}$ | 500 cycles | 3 lots, a total of 135 parts | 0 | JESD22-A104-B | AH102A-G |
| Stress or Test | Procedures/Conditions | Device <br> Hours/ <br> Cycles | Sample Size | Failed <br> Units | Reference <br> Document | Part <br> Tested |


| communication <br> 401 River Oaks Parkway <br> San Jose, CA 95134 | SIZE |  |  |
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| Unbiased Autoclave | Test Condition C <br> Temp. $121^{\circ} \mathrm{C}\left(+/-1^{\circ} \mathrm{C}\right)$ <br> Pressure $=15+/-1$ psig <br> Relative Humidity = 100\% | $96(-1,+5)$ hours | 3 lots, a total of 135 parts | 0 | JESD22-A102-C | AH102A-G |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Highly-Accelerated Temperature and Humidity Stress Test (HAST) | Test Condition A <br> Temp. $130^{\circ} \mathrm{C}\left(+/-2^{\circ} \mathrm{C}\right)$ <br> Pressure $=33.3+/-1$ psia <br> Relative Humidity $=85 \%$ | $96(-0,+2)$ hours | 3 lots, a total of 135 parts | 0 | JESD22-A110-B | AH102A-G |
| Solderability Lead-Free solder | Lead-Free Solder: Sn96Ag4 <br> Flux Type: R145 <br> Solder Bath Requirements: $260^{\circ} \mathrm{C}$ | N/A | 3 lots, a total of 10 parts, 30 pins | 0 | IPC/EIA/JEDEC J-STD-002B Method 2003) | AH102A-G |
| Solderability <br> Lead solder | Lead-Free Solder: Sn63Pb37 <br> Flux Type: R145 <br> Solder Bath Requirements: $245^{\circ} \mathrm{C}$ | N/A | 3 lots, a total of 10 parts, 30 pins | 0 | IPC/EIA/JEDEC <br> J-STD-002B <br> Method 2003) | AH102A-G |
| Moisture/Reflow <br> Sensitivity (MSL) <br> MSL level 3 lead free | Electrical test <br> External Visual C-SAM Die, Paddle and leads Dry Bake $125^{\circ} \mathrm{C}, 24$ hours $30^{\circ} \mathrm{C} / 60 \mathrm{RH}, 192$ hours <br> Convection reflow $260^{\circ} \mathrm{C}$, 3 X <br> External Visual <br> Electrical test <br> C-SAM Die, Paddle and leads | N/A | 1 lot, a total of 77 parts | 0 | J-STD-20C | AH102A-G |
| Unbiased High <br> Temperature Storage (HTB) | Temp. $150^{\circ} \mathrm{C}\left(+5^{\circ} \mathrm{C},-0^{\circ} \mathrm{C}\right)$ | 1000 hours | 1 lot, a total of 45 parts | 0 | JESD22-A103-C | AH102A-G |
| Physical Dimensions | N/A | N/A | 2 lots, a total of 2 parts | 0 | JESD22-B100-B | AH102A-G |
| High Temp Op Life (HTOL) | Test Condition B Temp. $125^{\circ} \mathrm{C}\left(+5,-0^{\circ} \mathrm{C}\right)$ | $\begin{aligned} & 1,000(-0,+10) \\ & \text { hours } \end{aligned}$ | 3 lots, a total of 135 parts | 0 | JESD22-A108-B | AH102A-G |

## V. DISCUSSION OF RESULTS

## 1. Testing procedures

All of the qualification tests were performed using loose parts except HAST and the HTOL which were mounted to a PCB. The PCB layout is the same as the application circuit published in the WJ Communications Data Sheet, including the recommended via pattern. The application circuit was duplicated 21 times on one large PCB for the qualification testing. A control board consisting of 21 devices was tested before and after each set of the stressed devices to ensure measurement accuracy and repeatability.

Components are considered to have failed if any of the following occurs after being tested post-stress and compared to respective pre-stressed testing parameters for the AG102A-G: any variation of $10 \%$ or greater for Supply Current, a variation of 1 dB or greater for Gain and a variation of 2 dB or greater for OIP3. Acceptance criterion consists of having zero failures out of 45 parts to meet WJ's requirement of LTPD $=5$ for each test.

## 2. Pre-Conditioning

Three lots of 208, a total of 624 AH102A-G devices, completed pre-conditioning with no electrical failures. 24 of the 624 devices underwent pre and post stress Scanning Acoustic Microscope inspection with no failures.


## 3. Temperature Cycle

A total of 135 AH102A-G devices from three lots completed 500 temperature cycles with no failures.
4. Unbiased Autoclave

A total of 135 AH102A-G devices from three lots completed Autoclave with no failures.
5. Highly Accelerated Temperature and Humidity (HAST)

A total of 135 AH102A-G devices from three lots completed HAST with no failures.
6. Solderability

See Solderability Test Report for WJ Products With Lead-Free Packaging Finish on the WJ web site.
7. Moisture/Reflow Sensitivity Classification (MSL)

A total of 77 AH102A-G devices from one lot completed MSL level 3 lead free testing with no failures. The MSL results are confirmed by the pre and post preconditioning Scanning Acoustic Microscope testing of 24 preconditioned AH102A-G devices underwent (MSL level 3 lead free profile, $260^{\circ} \mathrm{C}$ peak Temperature).
8. Unbiased High Temperature Storage (HTB)

A total of 45 AH102A-G devices from one lot completed 1000 hours of Unbiased High Temperature Storage with no failures.
9. Physical Dimensions

A total of 2 AH102A-G devices from two lots completed Inspection with no failures.
10. High Temp Op Life (HTOL)

A total of 135 AH102A-G devices from three lots completed 1,000 hours of HTOL with no failures.

## VII. CONCLUSIONS

The Reliability Qualification Data demonstrates that the AH102A-G device assembled in a lead-free/RoHScompliant/green SOT-89 surface-mount package demonstrates high reliability and quality levels. The entire MESFET/HFET amplifier family is also qualified in the lead-free/RoHS-compliant/green SOT-89 package by similarity. This includes the following device models: AG101-G, AG102-G, AG103-G, AH1-G, AH1-1G, AH2-G, AH3-G, AH31-G, AH101-G, AM1-G, FH1-G, FH101-G, FP101-G, FP1189-G, and FP2189-G.

| communications <br> 401 River Oaks Parkway <br> San Jose, CA 95134 | SIZE | 454221 |  |
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